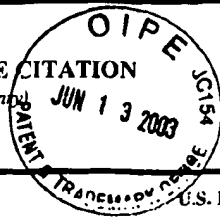


INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

100-15110 (P04976-P01)

Application Number

09/994,293

Applicant(s)

Abdalla Aly Naem

Filing Date

November 26, 2001

Group Art Unit

2823

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
G		5,323,032	06/21/94	Sato et al.	257	198	09/08/92
G		5,320,972	06/14/94	Wylie	437	31	01/07/93
G		4,564,997	01/21/86	Matsuo et al.	29	576W	04/16/82
G		6,352,907 B1	03/05/02	Gris	438	438	05/26/00
G		6,171,936 B1	01/09/01	Fitzgerald	438	503	01/04/99
G		5,897,359	04/27/99	Cho et al.	438	312	12/09/97

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
G		JP04082268	03/16/92	Japan	H01L	27/14		✓
G		JP02260430	10/23/90	Japan	H01L	21/331		✓
G		JP06318602	11/15/94	Japan	H01L	21/331		✓
G		EP0779,663 A2	06/18/97	Europe	H01L	29/732		✓

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

G		IBM Technical Disclosure Bulletin, "Epi-Based Bipolar Transistor With Oxide-Defined Collector Window", Vol. 34, No. 1, (June 1991), pp. 422-424.

EXAMINER

Farson

DATE CONSIDERED

4/22/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.